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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
														YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

✓		Sohi, G.S., "Instruction Issue Logic For High-Performance, Interruptible, Multiple Functional Unit, Pipeline Computers, IEEE Transactions on Computers, Vol. 39, No. 3, March 1990, pp 349-359
✓		Fisher, J.A., "Very Long Instruction Word Architectures and ELI-512" Proceedings of the 10th International Symposium on Computer Architecture 1983
✓		R. Hartenstein, " Coarse Grain Reconfigurable Architectures", ASP-DAC 2001, pp 564-569

EXAMINER	DATE CONSIDERED 5/APRIL/2006

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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